

*Purchaser.* The following table sets forth the name, business address and telephone number, citizenship, present principal occupation, employment and academic history, material occupations, positions, offices or employment for at least the past five years of each of the managers of Purchaser. The current business address of each of the managers is 2200 Mission College Boulevard, Santa Clara, California, 95054, and the current business telephone number is (408) 765-8080.

<u>Name</u>	<u>Citizenship</u>	<u>Present Principal Occupation or Employment</u>
Brian M. Krzanich	United States	<b>Function:</b> Manager

**Professional Background:** Brian M. Krzanich has served as manager of Purchaser since the Conversion. Mr. Krzanich was appointed chief executive officer of Intel Corporation and elected a member of the board of directors of Intel Corporation on May 16, 2013. He is the sixth CEO in the company's history, succeeding Paul S. Otellini.

Krzanich has progressed through a series of technical and leadership roles at Intel, most recently serving as the chief operating officer (COO) from 2012 to 2013. As COO, his responsibilities included leading an organization of more than 50,000 employees spanning Intel's Technology and Manufacturing Group, Intel Custom Foundry, supply chain operations, the NAND Solutions group, human resources, information technology, and Intel's China strategy.

His open-minded approach to problem solving and listening to customers' needs has extended the company's product and technology leadership and created billions of dollars in value for the company. In 2006, he drove a broad transformation of Intel's factories and supply chain, improving factory velocity by more than 60 percent and doubling customer responsiveness. Krzanich is also involved in advancing the industry's transition to lower cost 450mm wafer manufacturing through the Global 450 Consortium as well as leading Intel's strategic investment in lithography supplier ASML.

Prior to becoming COO, Krzanich held senior leadership positions within Intel's manufacturing organization. He was responsible for Fab/Sort Manufacturing from 2007-2011 and Assembly and Test from 2003 to 2007. From 2001 to 2003, he was responsible for the implementation of the 0.13-micron logic process technology across Intel's global factory network. From 1997 to 2001, Krzanich served as the Fab 17 plant manager, where he oversaw the integration of Digital Equipment Corporation's semiconductor manufacturing operations into Intel's manufacturing network. The assignment included building updated facilities as well as initiating and ramping 0.18-micron and 0.13-micron process technologies. Prior to this role, Krzanich held plant and manufacturing manager roles at multiple Intel factories.

Krzanich began his career at Intel in 1982 in New Mexico as a process engineer. He holds a bachelor's degree in Chemistry from San Jose State University and has one patent for semiconductor processing. Krzanich is also a member of the board of directors of the Semiconductor Industry Association.